PATENT NUMBER

U.S. **UTILITY** Patent Application

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APPLICATION NO. CONT/PRIOR CLASS SUBCLASS ART UNIT EXAMINER
09/971958 F 257 2814

Motoshige Igarashi Hiroyuki Amishiro

Semiconductor device and method for manufacturing the same

TITLE

ISSUING CLASSIFICATION							
ORIGIN	IAL	CROSS REFERENCE(S)					
CLASS SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONA	L CLASSIFICATION	7					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
The term of this patent subsequent to (date)				NOTICE OF ALLOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)	•	
The term of this patent shall not extend beyond the expiration date					
of U.S Patent. No.				ISSUE FEE	
				Amount Due	Date Paid
	(Primary E	xaminer)	(Date)		
The terminalmonths of this patent have been disclaimed.			, , , , , , , , , , , , , , , , , , ,	ISSUE BATCH NUMBER	
this patent have been disclashed.	(Legal Instrume	nts Examiner)	(Date)		
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